

‘H’

V1.1

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WOL Magic Packet 1

1

1	γ	'H	≠		1
2	γ	'H	∇		1
2.1	X86	[OBJ]	∇ ∇	γ	1
2.2	ARM	[OBJ]	∇ ∇	γ	2
2.2.1	'H	∇			2
3	∇ ∇	γ		∇	4
4	∇ ∇	γ		∇	5
5	ǎ ǎ	6			5

[illegible]

2-3

DC 3
Q [OBJ]

↑ 35

CPU

PHY 303

4

$$\tau \equiv \tilde{\omega}$$

4

PHY 5

